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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/804,024
Filing Date	3/19/2004
First Named Inventor	Charles Douglas Murphy
Art Unit	2819
Examiner Name	J. Cho
Attorney Docket Number	

Sheet 1 of 2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
JL		US- 6587145	07-01-2003	A. Hou	
		US- 5461 425	10-24-1995	B. Fonder & A. El Gamal	
		US- 5419 208	12-26-1995	F. Okumura	
		US- 6642503	11-04-2003	R. K. Kumaraguru et al.	
		US- 6559 178	05-06-2003	C. D. Murphy	
		US- 5650643	07-22-1997	K. Konung	
		US- 6680498	01-20-2004	R. Guidash	
		US- 6069 777	05-30-2000	W. F. Prentice & R. Guidash	
		US- 6307 195	10-23-2001	R. Guidash	
JL		US- 6552 745	04-22-2003	F. A. Perner	
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FOREIGN PATENT DOCUMENTS

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		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				

Examiner Signature	James Cho	Date Considered	8/9/06
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		Application Number	10/804,024		
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		First Named Inventor	CHARLES DOUGLAS MURPHY		
		Art Unit	2819		
		Examiner Name	J. Cho		
Sheet	2	of	2	Attorney Docket Number	

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
J		PAUL HOROWITZ + WENFELD HELL, <u>The Art of Electronics</u> , pages 621-630, 4th edition, 1994, Cambridge University Press, New York	Numerical
J		JENCHEOL YOO, KYUSUN CHOI, + ALE TANGEL, "A 1 GSps CMOS Flash Converter for System-on-Chip Applications", IEEE Computer Society Workshop on VLSI, April 19-20, 2001.	

Examiner Signature	James Cho	Date Considered	8/9/06
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